



SPECIFICATION

MODULE NO.: WF70UTIAHLNNO#

General Specifications

Item	Dimension	Unit
Size	7.0	inch
Dot Matrix	1024 x RGBx600(TFT)	dots
Module dimension	164.9(W) x 100(H) x 4.8(D)	mm
Active area	154.2114 x 85.92	mm
Dot pitch	0.1506 x 0.1432	mm
LCD type	TFT, Normally White, Transmissive	
View Direction	12 o'clock	
Gray Scale Inversion Direction	6 o'clock	
Backlight Type	LED, Normally White	
With /Without TP	Without TP	
Interface	LVDS	
Surface treatment	Hard Coating	
Surface	Glare	

*Color tone slight changed by temperature and driving voltage.

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	°C
Storage Temperature	TST	-30	—	+80	°C

Electrical Characteristics

Typical Operation Conditions

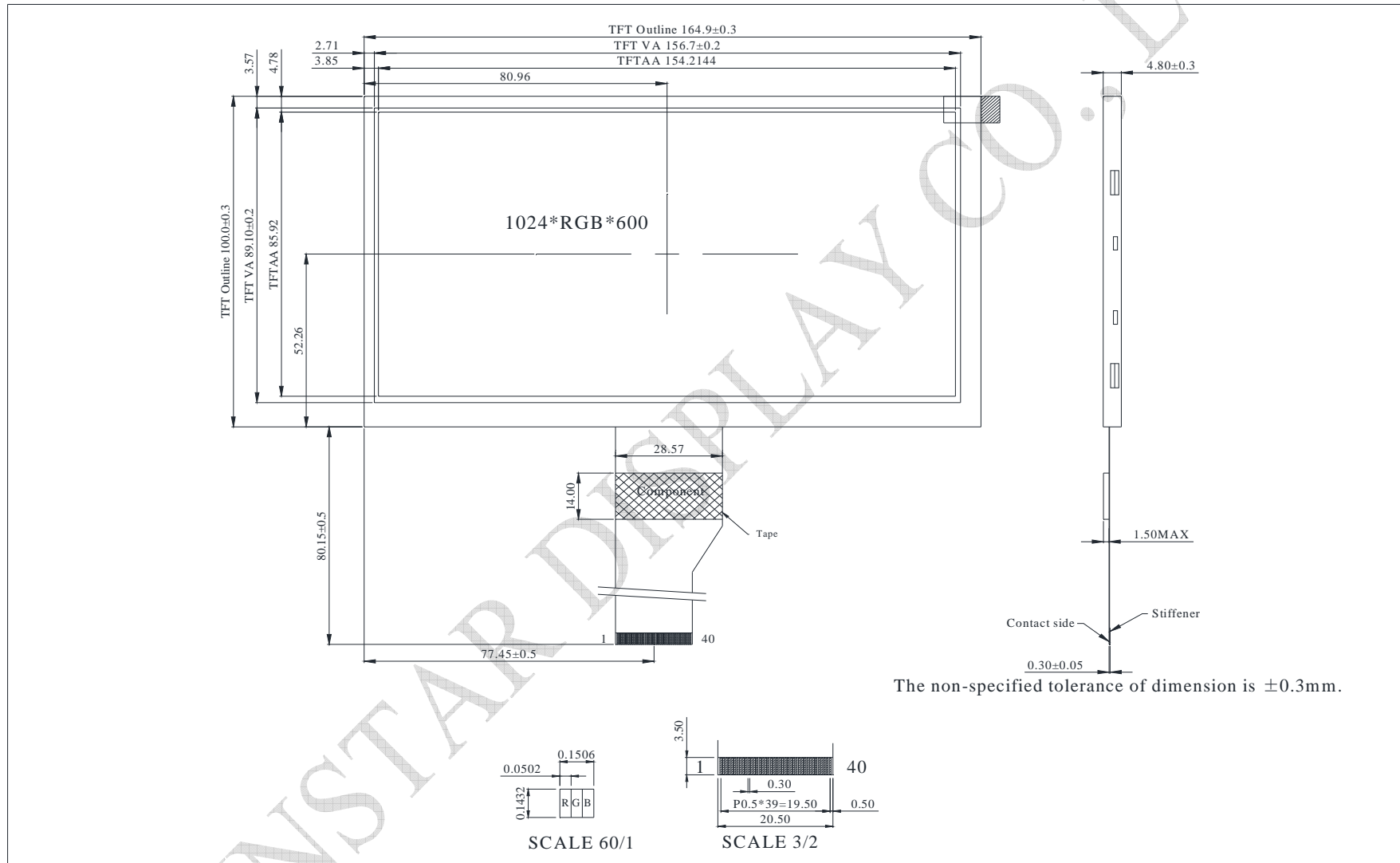
Item	Symbol	Values			Unit
		Min.	Typ.	Max.	
Power voltage	DVDD	3.0	3.3	3.6	V
	AVDD	10.8	11	11.2	V
	VGH	19.7	20	20.3	V
	VGL	-6.5	-6.8	-7.1	V
Input signal voltage	VCOM	3.3	3.8	4.3	V
Input logic high voltage	VIH	0.7 DVDD	-	DVDD	V
Input logic low voltage	VIL	0	-	0.3 DVDD	V

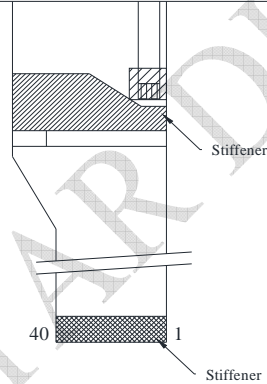
Interface

Pin No.	Symbol	I/O	Function
1	VCOM	P	Common Voltage
2	VDD	P	Power Voltage for digital circuit
3	VDD	P	Power Voltage for digital circuit
4	NC	---	No connection
5	Reset	I	Global reset pin
6	STBYB	I	Standby mode, Normally pulled high STBYB = "1", normal operation STBYB = "0", timing controller, source driver will turn off, all output are High-Z
7	GND	P	Ground
8	RXIN0-	I	- LVDS differential data input
9	RXIN0+	I	+ LVDS differential data input
10	GND	P	Ground
11	RXIN1-	I	- LVDS differential data input
12	RXIN1+	I	+ LVDS differential data input
13	GND	P	Ground
14	RXIN2-	I	- LVDS differential data input
15	RXIN2+	I	+ LVDS differential data input
16	GND	P	Ground
17	RXCLKIN-	I	- LVDS differential clock input
18	RXCLKIN+	I	+ LVDS differential clock input
19	GND	P	Ground
20	RXIN3-	I	- LVDS differential data input
21	RXIN3+	I	+ LVDS differential data input
22	GND	P	Ground
23	NC	---	No connection
24	NC	---	No connection
25	GND	P	Ground
26	NC	---	No connection

27	DIMO	O	Backlight CABC controller signal output
28	SELB	I	6bit/8bit mode select
29	AVDD	P	Power for Analog Circuit
30	GND	P	Ground
31	LED-	P	LED Cathode
32	LED-	P	LED Cathode
33	L/R	I	Horizontal inversion
34	U/D	I	Vertical inversion
35	VGL	P	Gate OFF Voltage
36	CABCEN1	I	CABC HW enable
37	CABCEN0	I	CABC HW enable
38	VGH	P	Gate ON Voltage
39	LED+	P	LED Anode
40	LED+	P	LED Anode

Contour Drawing





PIN NO.	SYMBOL	PIN NO.	SYMBOL
1	VCOM	21	RXIN3+
2	VDD	22	GND
3	VDD	23	NC
4	NC	24	NC
5	Reset	25	GND
6	STBYB	26	NC
7	GND	27	DIMO
8	RXIN0-	28	SELB
9	RXIN0+	29	AVDD
10	GND	30	GND
11	RXIN1-	31	LED-
12	RXIN1+	32	LED-
13	GND	33	L/R
14	RXIN2-	34	U/D
15	RXIN2+	35	VGL
16	GND	36	CABCEN1
17	RXCLKIN-	37	CABCEN0
18	RXCLKIN+	38	VGH
19	GND	39	LED+
20	RXIN3-	40	LED+

The non-specified tolerance of dimension is $\pm 0.3\text{mm}$.